Abstract

A current is provided from a power source to a load through a pass circuit that is series coupled to a sense resistor. A current trip-point detection circuit is arranged to detect a change in the current that is provided to a load. The current trip-point detection circuit includes at least two resistors that are series coupled from the sense resistor to a current source. A comparator compares a sense voltage to a tap-point between the two resistors such that the comparator asserts a trip-point detection signal when the current to the load reaches a predetermined threshold. The sense voltage can correspond to the voltage across the load or some other voltage that is proportional to the voltage across the load. The circuit arrangement has a simplified design that sets the trip-point as a percentage of the maximum output current. The current level trip-point can be temperature compensated.

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